

FIG. 1  
Prior Art

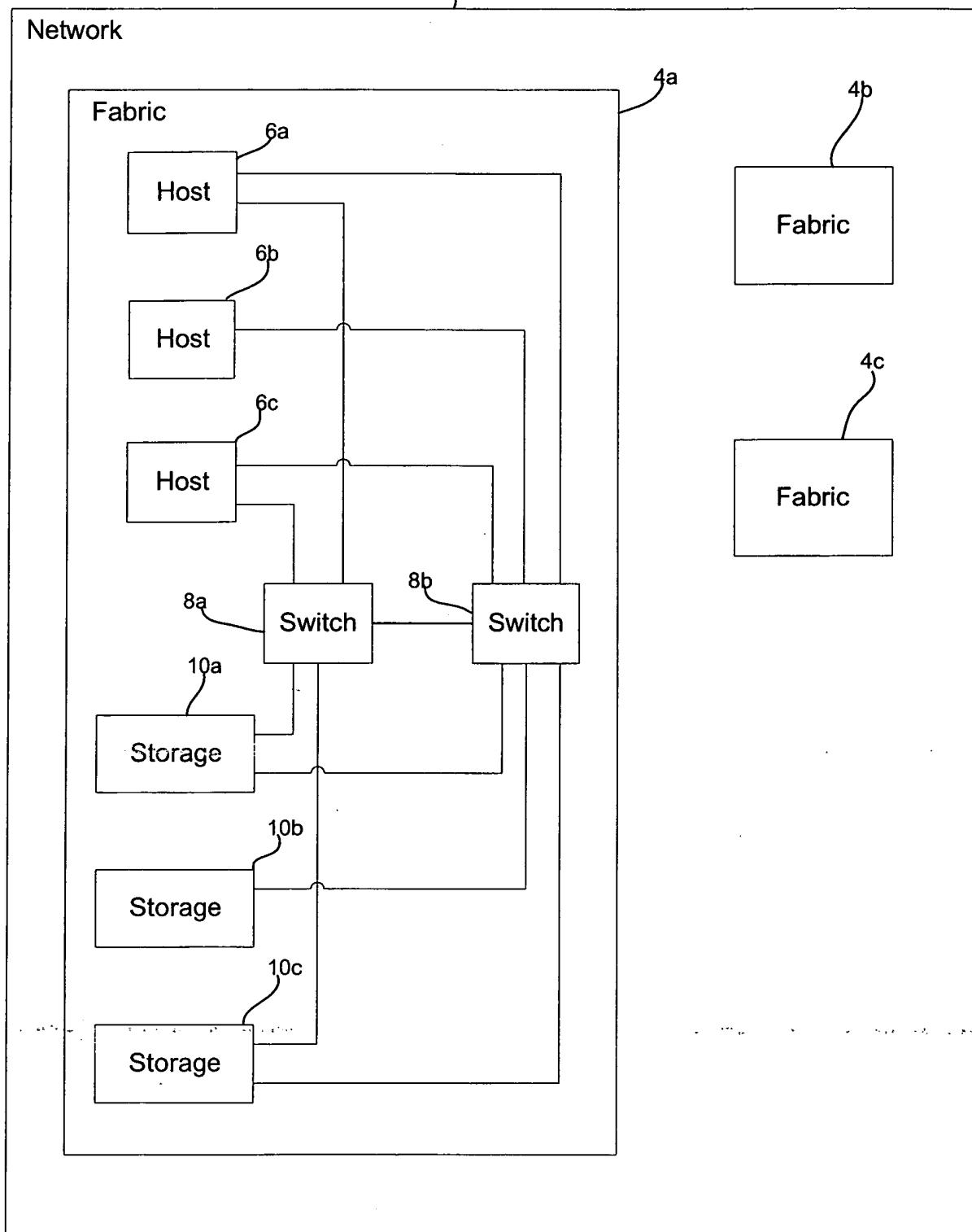


FIG. 2

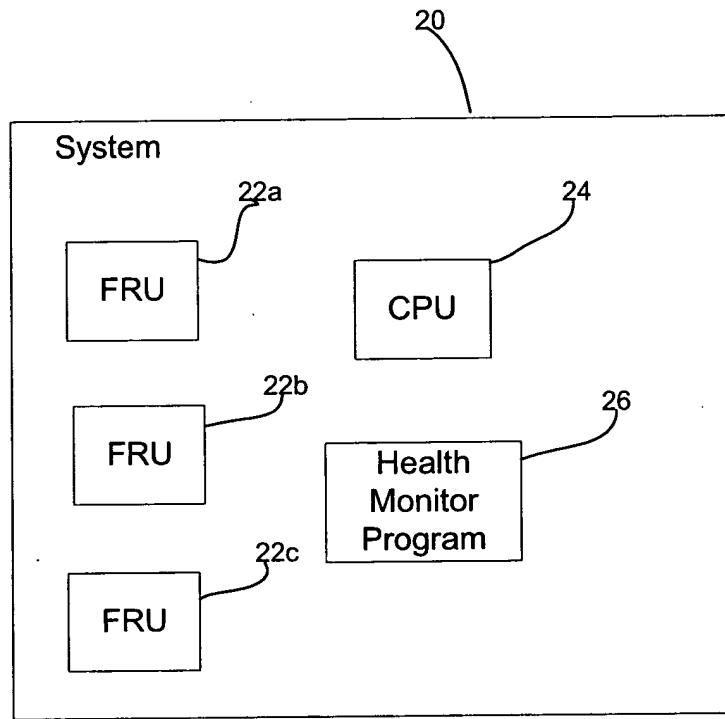


FIG. 3

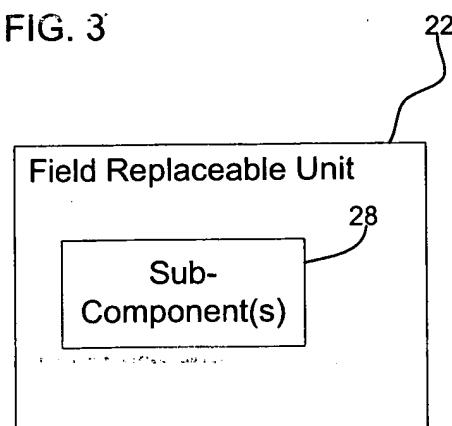


FIG. 4

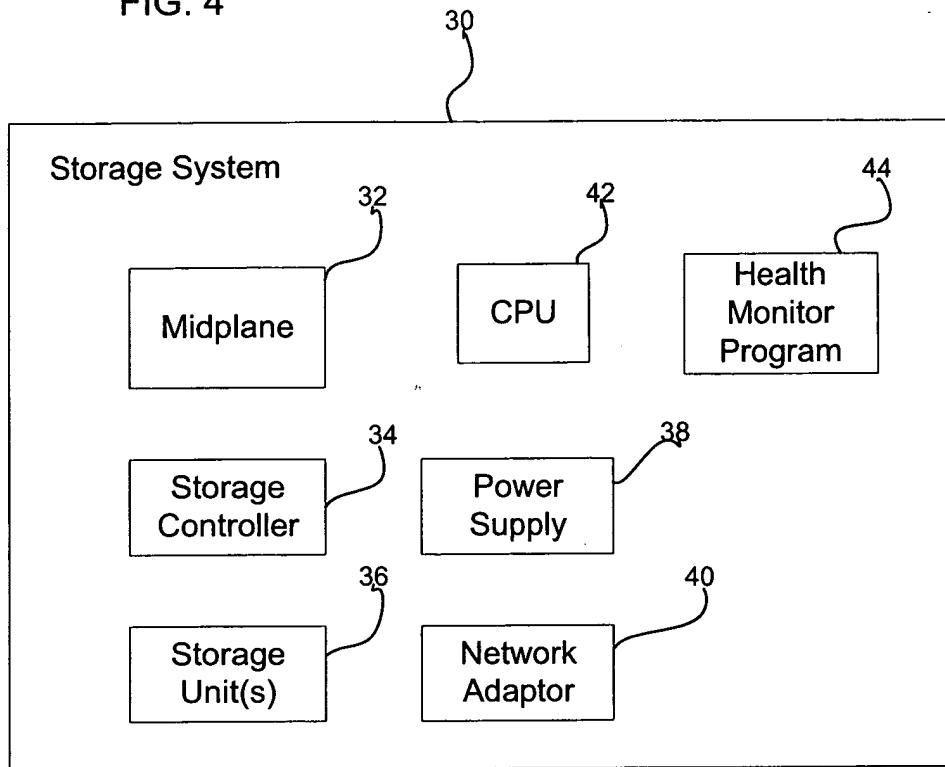


FIG. 5

Truth Table for Power Supply

<i>PS_State</i>	<i>Temp_Status</i>	<i>Battery_Status</i>	<i>Fan_Status</i>	<i>Output State</i>
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	D
0	1	0	0	D
0	1	0	1	D
0	1	1	0	D
0	1	1	1	I
I	0	0	0	0
I	0	0	1	0
I	0	1	0	0
I	0	1	1	D
I	1	0	0	D
I	1	0	1	D
I	1	1	0	D
I	1	1	1	I

FIG. 6

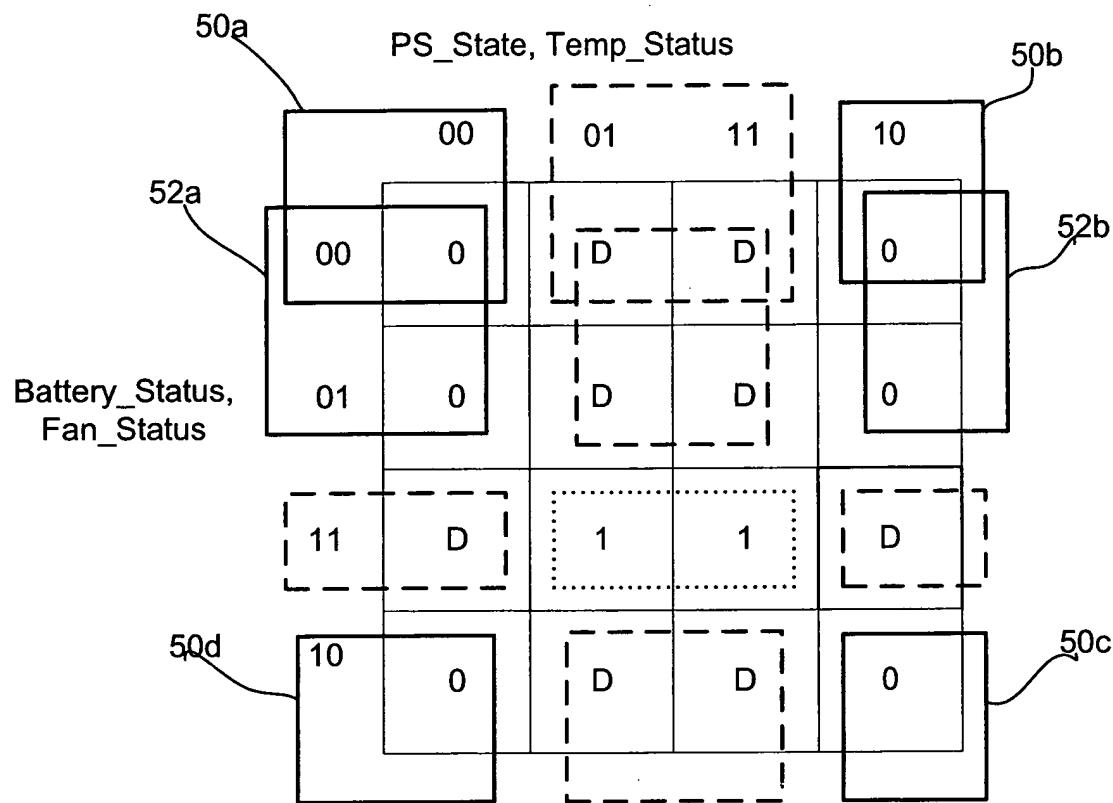


FIG. 7

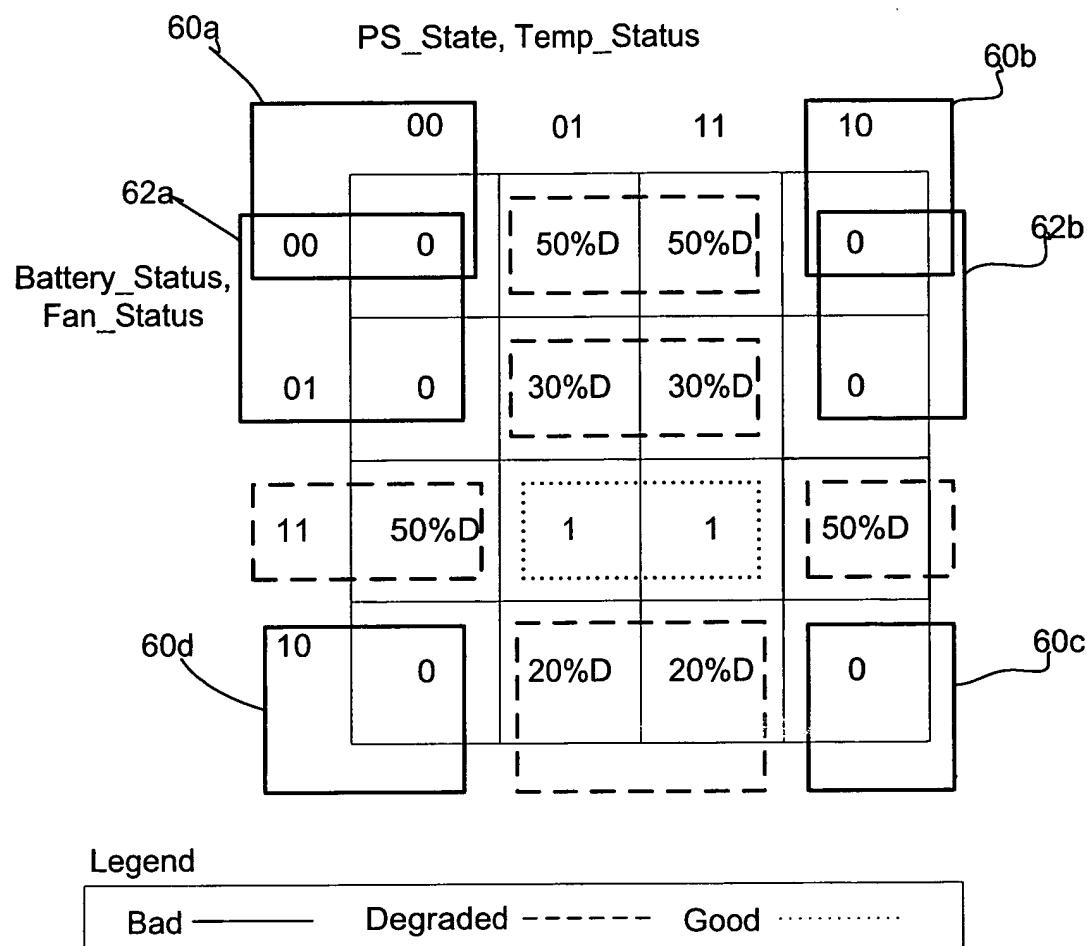
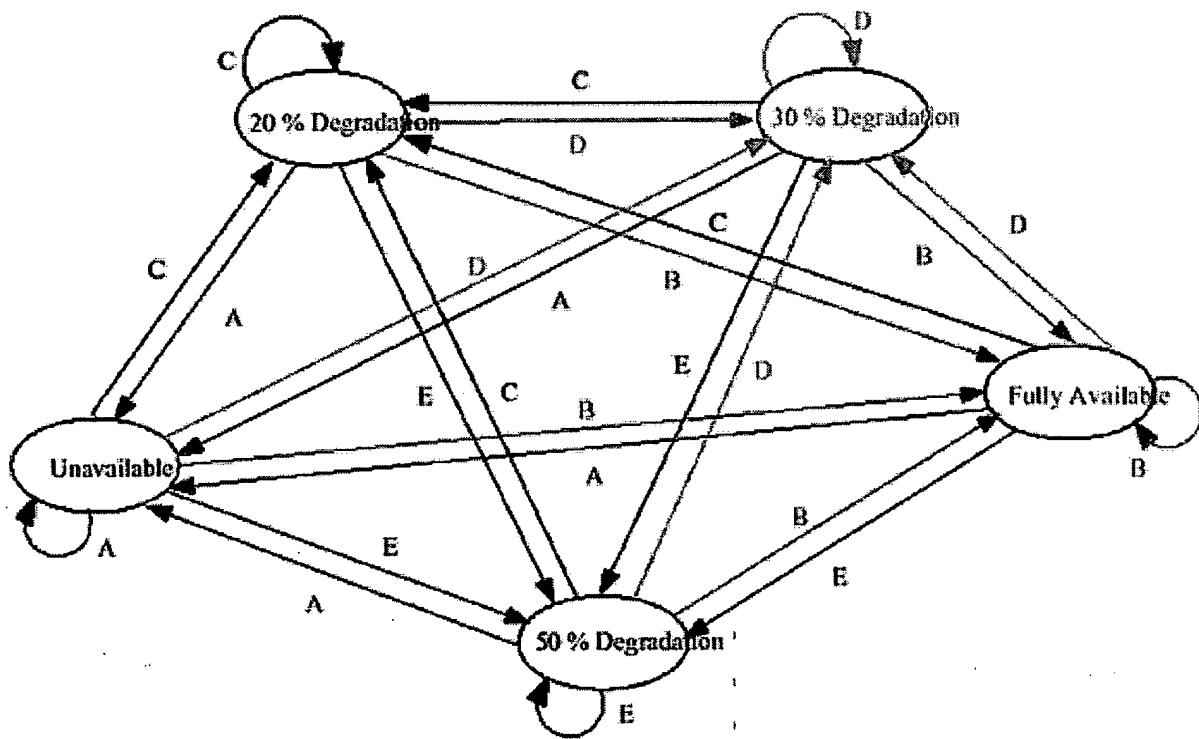


FIG. 8



Equation A (Power Supply is Bad)	$[\text{Temp\_Status}] * [\text{Battery\_Status} * \text{Fan\_Status}]$
Equation B: (Power Supply is Good)	$\text{Temp\_Status} * \text{Battery\_Status} * \text{Fan\_Status}$
Equation C (20% Degradation in Power Supply)	$\text{Temp\_Status} * \text{Battery\_Status} * [\text{Fan\_Status}]$
Equation D (30% Degradation in Power Supply)	$\text{Temp\_Status} * [\text{Battery\_Status}] * \text{Fan\_Status}$
Equation E (50% Degradation in Power Supply)	$[(\text{Temp\_Status} * \text{Battery\_Status} * \text{Fan\_Status}) + (\text{Temp\_Status} * [\text{Battery\_Status}] * \text{Fan\_Status})]$

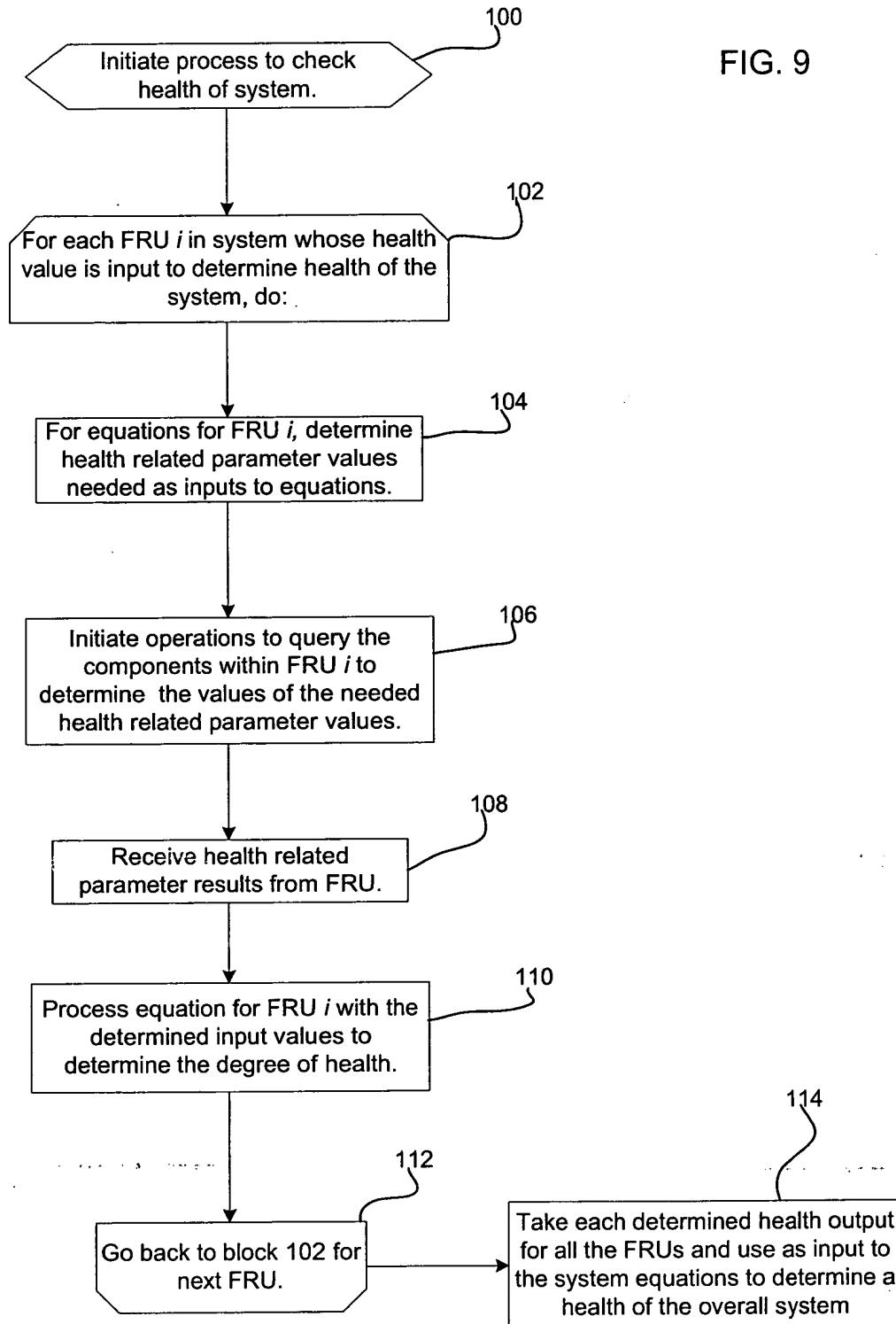


FIG. 10

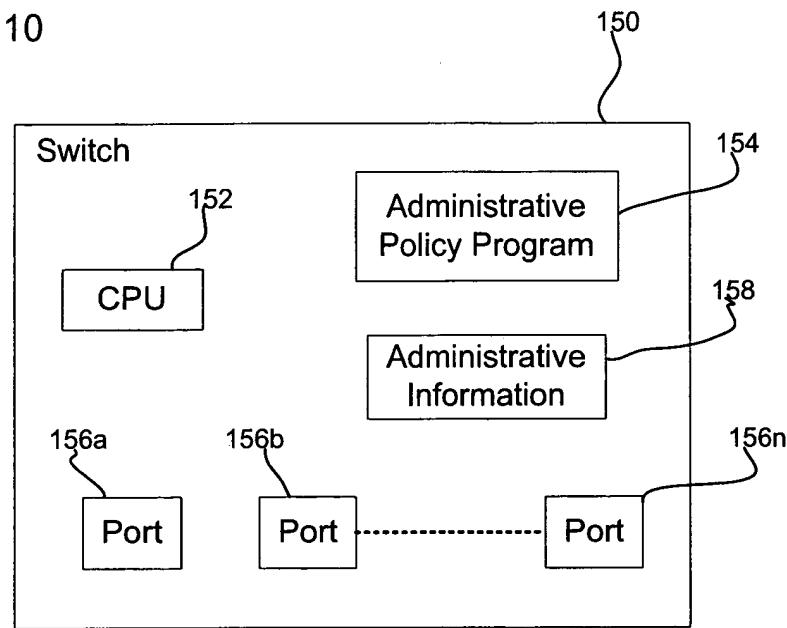


FIG. 11

Administrative Policy Truth Table

<i>Previous Admin State</i>	<i>Previous Oper State</i>	<i>Current Admin State</i>	<i>Current Oper State</i>	<i>Alert Clear</i>	<i>Alert Create</i>	<i>Event</i>
0	0	0	0	0	0	0
0	0	0	1	0	0	1
0	0	1	0	0	0	1
0	0	1	1	1	0	1
0	1	0	0	0	0	1
0	1	0	1	0	0	0
0	1	1	0	0	1	1
0	1	1	1	0	0	1
1	0	0	0	0	0	1
1	0	0	1	0	0	1
1	0	1	0	0	0	0
1	0	1	1	1	0	1
1	1	0	0	0	0	1
1	1	0	1	0	0	1
1	1	1	0	0	1	1
1	1	1	1	0	0	0

# USING AND GENERATING FINITE STATE MACHINES TO MONITOR SYSTEM STATUS

Inventor: K. Desai; Docket No. P8966

Sheet 11/12

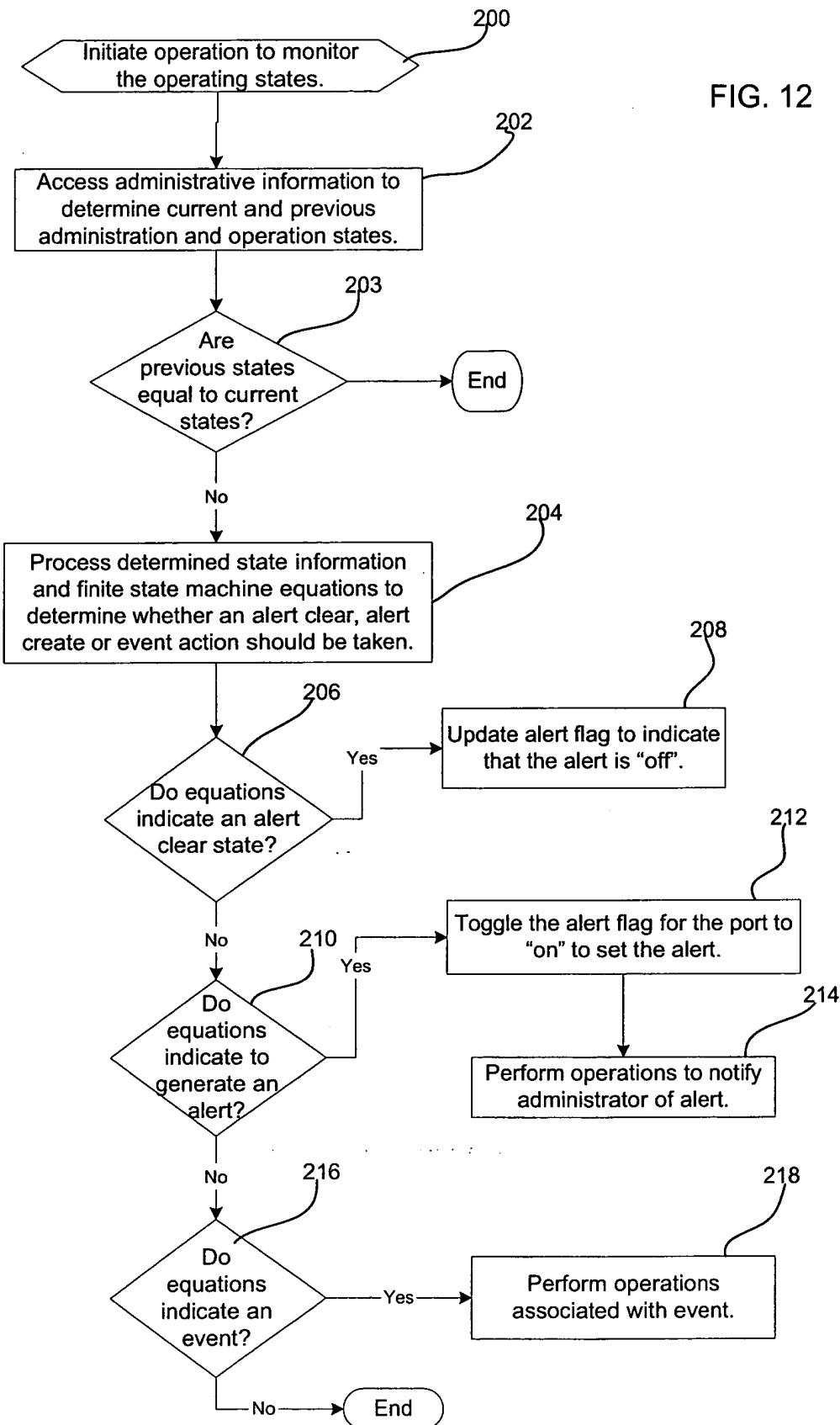


FIG. 12

FIG. 13

